

<b>Notice of References Cited</b>	Application/Control No. 10/668,558	Applicant(s)/Patent Under Reexamination STASIAK ET AL.	
	Examiner Howard Weiss	Art Unit 2814	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., et al., Silicon Processing For the VLSI Era: Processing Technology, 1986, Lattice Press, Vol. 1, p.124
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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